

SIEMENS
SALMANY

Bit Logic Instruction

- A And •
- AN And Not •
- O Or •
- ON OrNot •
- X Exclusive Or •
- XN Exclusive Or •
- O And before Or •
- A(And with Nesting Open •
- AN(And Not with Nesting Open •

SINA

SIEMENS
SALMANY

AND

Circuit Diag.

LAD

FBD

STL

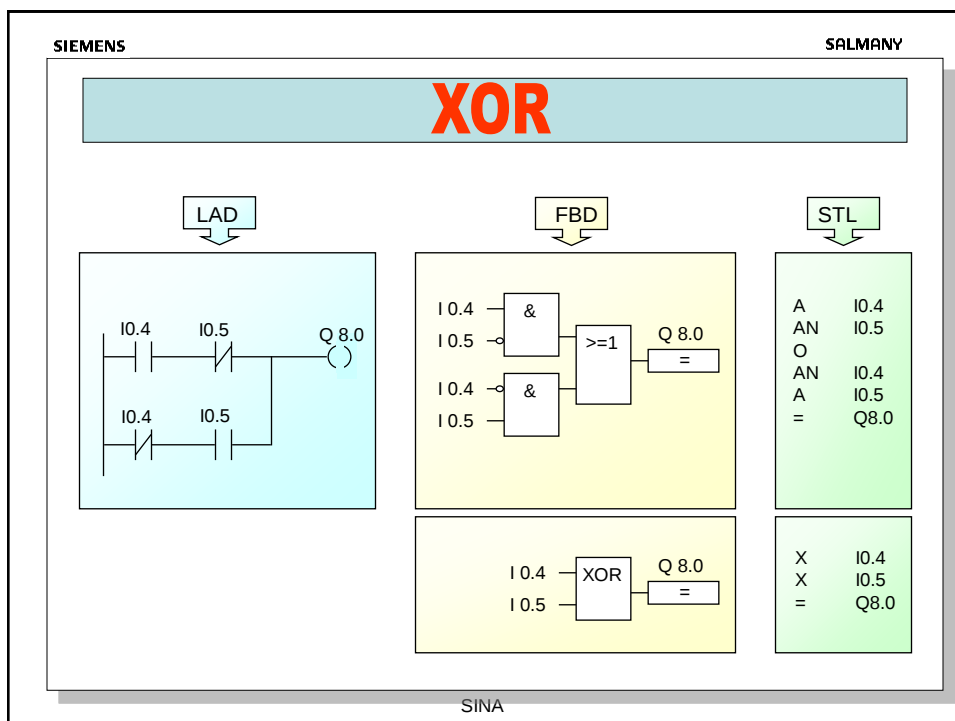
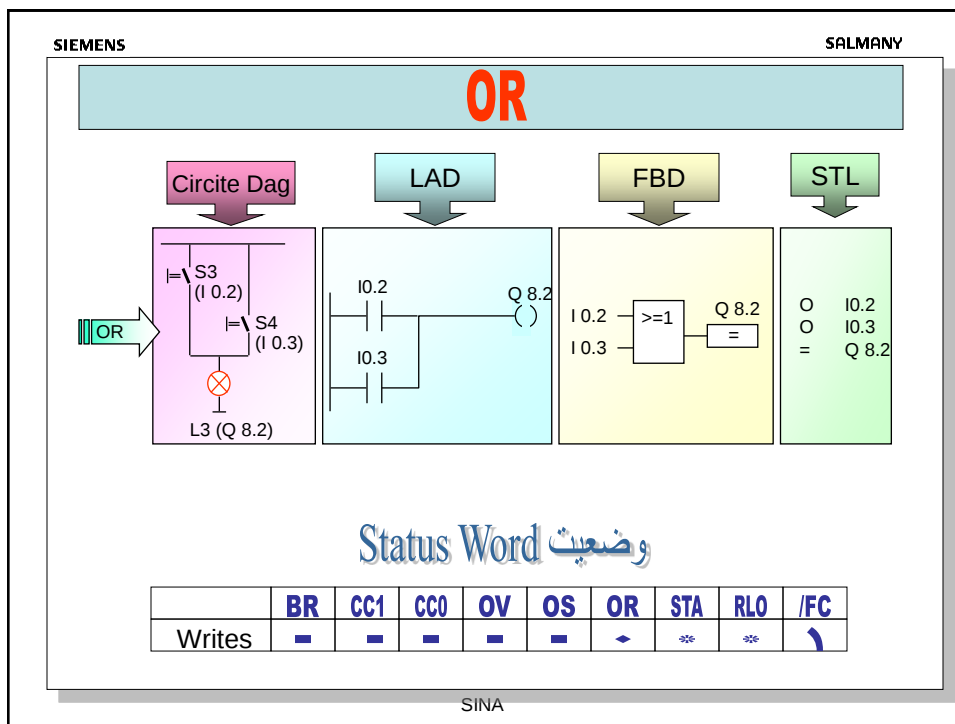
```

A    I0.0
A    I0.1
=    Q 8.0
=    Q 8.1
        
```

وضعیت Status Word

	BR	CC1	CC0	OV	OS	OR	STA	RLO	/FC
Writes	■	■	■	■	■	■	■	■	■

SINA



SIEMENS
SALMANY

XOR

وضعیت Status Word

	BR	CC1	CC0	OV	OS	OR	STA	RLO	/FC
Writes	—	—	—	—	—	◀	※	※	↶

SINA

SIEMENS
SALMANY

Set , Reset

LAD

FBD

STL

Assignment

Set

Reset

		<pre> A I1.0 A I1.1 = Q8.0 </pre>
		<pre> A I1.2 A I1.3 S Q8.1 </pre>
		<pre> O I1.4 O I1.5 R Q8.1 </pre>

SINA

SIEMENS
SALMANY

Set , Reset

SINA

SIEMENS
SALMANY

Setting / Resetting a Flip Flop

LAD

FBD

STL

```

A I 1.2
S M 0.0
A I 1.3
R M 0.0
A M 0.0
= Q 9.3
                    
```

LAD

FBD

STL

```

A I 1.3
R M 0.0
A I 1.2
S M 0.0
A M 0.0
= Q 9.3
                    
```

SINA

